FEB 2 6 2001 W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

3/6/01

Docket No. 0756-155

GAU 282

In re PATENT application of:)			
Shunpei YAMAZAKI et al.)	Art Unit: 2822		
Serial No. 08/691,434)	Examiner: M. Wilc	zewski	
Filed: 08/02/1996)			
For: METHOD OF FABRICATING)			
SEMICONDUCTOR DEVICES AND)		31	
APPARATUS FOR PROCESSING A	.)		HAR 2800	7
SEMICONDUCTOR)			CF
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sufficient postage as Eight Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 2/22/2001.

I hereby certify that this correspondence is being deposited with the United States Postal Service with

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Japanese Patent Laid-Open Nos. 60-066471, 64-057615, 04-240733, and 05-254984 were cited in the priority Japanese Application No. 5-301,172.

Japanese Patent Laid-Open Nos. 63-250178, 03-293719, 04-152624, 04-373178, 05-243577, 05-251342, 06-084944, and 07-099321 were cited in the family Japanese Application No. 6-100,642.

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The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

effrey L. Costellia

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